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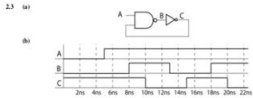


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Chapter 2 Solutions

- 2.1 (a) VHDL: Hardware Description Language
VHDL: Very High Speed Integrated Circuit
- (b) VHDL has statements that execute concurrently, since it must model real hardware in which the components are all operations at the same time.
- (c) A hardware description language allows a digital system to be designed and debugged at a higher level of abstraction than schematic capture with gates, flip-flops, and standard MSI building blocks. The details of the gates and flip-flops do not need to be modeled during early phases of design. Designs are more portable when low-level library-specific details are not included in the model. HDLs allow the creation of such portable high-level behavioral models.
- 2.2 (a) Legal: 6, 123, 4567, 8901 (chars with number, all 22 chars with underscores, 1234, 5678 with underscores), 9, 10 (adjacent underscores), 11 (space of word)
- (b) They all represent VHDL, in not case sensitive.



```
2.4 entity comb_2a
  port (A, B, C : in bit;
        out : out bit);
architecture comb_2a of comb_2a
  signal F, G, H, I, J, K;
begin
  B <= not A and (not A and B) and B;
  C <= not A and B and not B;
  F <= not A and B and not B;
  G <= not A and B and B;
  H <= not A and B and B;
  I <= not A and B and B;
  J <= not A and B and B;
  K <= not A and B and B;
end comb_2a;
```

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